

EDAA Lifetime Achievement Award 2009 goes to Jan M. RABAEY

Grenoble, France, 10 January 2009 – The EDAA Lifetime Achievement Award 2009 goes to Jan M. RABAEY.

The Lifetime Achievement Award is given to individuals who made outstanding contributions to the state of the art in electronic design, automation and testing of electronic systems in their life. In order to be eligible, candidates must have made innovative contributions which had an impact on the way electronic systems are being designed.

Past recipients have been Kurt ANTREICH (2003), Hugo DE MAN (2004), Jochen JESS (2005), Robert BRAYTON (2006) and Tom W. WILLIAMS (2007) and Ernest S. KUH.

The Award will be presented at the plenary session of the 2009 DATE Conference, to be held 20-24 April in Nice, France.



Jan M. RABAEY received the EE and Ph.D degrees in applied sciences from the Katholieke Universiteit Leuven, Belgium, respectively in 1978 and 1983. From 1983 till 1985, he was connected to the University of California, Berkeley as a Visiting Research Engineer. From 1985 till 1987, he was a research manager at IMEC, Belgium, and in 1987, he joined the faculty of the Electrical Engineering and Computer Science department of the University of California, Berkeley, where he is now holds the Donald O. PEDERSON Distinguished Professorship. He has been a visiting professor at the University of Pavia (Italy), Waseda University (Japan), Technical University Delft (Netherlands), Victoria Technical University and the University of New South Wales (Australia). He was the Associate Chair (EE) of the EECS Dept. at Berkeley from 1999 till 2002, and is currently the Scientific co-director of the Berkeley Wireless Research Center (BWRC, as well as the director of the GigaScale Systems Research Center (GSRC).

Jan M. RABAEY authored or co-authored a wide range of papers in the area of signal processing and design automation. He received numerous scientific awards, including the 1985 IEEE Transactions on Computer Aided Design Best Paper Award (Circuits and Systems Society), the 1989 Presidential Young Investigator award, and the 1994 Signal Processing Society Senior Award, and the 2002 ISSCC Jack Raper Award. In 1995, he became an IEEE Fellow. He is past chair of the VLSI Signal Processing Technical Committee of the Signal Processing Society and chaired the executive committee of the Design Automation Conference. He is serving on the Technical Advisory Board of a wide range of companies.

His current research interests include the conception and implementation of next-generation integrated wireless systems. This includes the analysis and optimization of communication algorithms and networking protocols, the study of low-energy implementation architectures and circuits, and the supporting design automation environments.

More information and electronic version of this PR available at:

<http://www.edaa.com/>

B. COURTOIS, EDAA Chair, tel : +33 476 57 46 15, Bernard.Courtois@imag.fr

EDAA is a non-profit association. Its purpose is to operate for educational, scientific and technical purposes for the benefit of the international electronics design and design automation community. The Association, in the field of design and design automation of electronic circuits and systems, promotes a series of high quality technical international conferences and workshops across Europe and cooperates actively to maintain harmonious relationships with other national and international technical societies and groups promoting the purpose of the Association. EDAA is the main sponsor of DATE, the premier Design, Automation and Test Conference and Exhibition in Europe.